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AUG 10 2007

Docket No.: 4444-0120P
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Chin LEE

Application No.: 10/620,456

Confirmation No.: 9179

Filed: July 17, 2003

Art Unit: 2114

For: MEMORY MODELING CIRCUIT WITH
FAULT TOLERATION

Examiner: P. F. Contino

AMENDMENT IN RESPONSE TO NON-FINAL OFFICE ACTION

MS AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

OK TO ENTER: /PFC/

08/14/2007

Sir:

INTRODUCTORY COMMENTS

In reply to the Office Action dated May 10, 2007, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

This reply includes:

Amendments to the Claims; and

Remarks.

Birch, Stewart, Kolasch & Birch, LLP

KM/ASC/